



Common Entrance Test Registration Form

Affix your
passport size
photograph

PG Diploma Course in Embedded System Design
(Course duration – 12 Months, full time)

Name :
(Write your name as you want it to be printed on the Certificate)

Address :

 Pin Code

Mobile No. : E-mail:

Date of Birth : Sex: M F

Are you interested in pursuing MS programme of ISEP at Paris? Yes No

Father's Name: Mother's Name:

Occupation: Mobile No.:

E-mail ID:

Educational Qualification: (Attach Photocopy of Mark Sheet / Certificate of Highest Qualification)

Examination	Year of passing	% of marks	Name of College	University / Board
Master's Degree (Engg./ Science)				
Bachelor Degree (Engineering)				
3 rd year (Engg.) / Bachelor (Science)				
2 nd year (Engg. / Science)				
1 st year (Engg. / Science)				
Diploma (Engg.)				
H.S.C. (12 th Standard)				
S.S.C. (10 th Standard)				

Category of applicant: Open SC ST DTNT OBC

Reserved category students please enclose attested photocopy of Caste Certificate and Validity Certificate.

I hereby declare that the information given above is true to the best of my knowledge. I have read the selection procedure set forth in the prospectus and related official announcements. I declare that I accept the terms and conditions set forth herein. I recognize the financial obligations & payment dates as stated.

Date: Applicant's Signature

Entrance Exam Fee: Rs. 500/- only (Pay by Cash or DD in favour of "ICIT Pvt. Ltd., Pune")

Demand Draft No.: Date: Bank:

Application Form

**ONLY FOR THE INFORMATION OF STUDENT
(DO NOT SEND WITH THE APPLICATION FORM)**

Fee schedule	PG Diploma Course in Embedded System Design
Fee Installment	Amount (Rs.)
Confirmation of Admission	Rs. 50,000/-
1 st installment (within 30 days of course commencement)	Rs. 35,000/-
Total course fee	Rs. 85,000/-

CET Details and Admission Procedure

Issue of Hall Tickets: Hall tickets will be sent by e-mail after receiving completed application forms and DD (which should then be printed by the student). It is compulsory for student to carry the hall ticket to the testing center. Address of the testing center will be printed on the hall ticket.

The preferred date and time mentioned by the candidate for a testing centre will be considered depending upon the availability and if the application forms are received on time, otherwise the next available date and time at that centre will be provided by ICIT which has to be accepted by the candidate. This will be notified to the candidate via email.

CET fee is Rs. 500.00 and may be paid:

a) By Cash in ICIT office.

OR

b) By Demand Draft – payable to “ICIT Pvt. Ltd., Pune”.

Kindly send by speed post/courier along with the filled Application form.

OR

c) In ICIT bank account: (send a scanned image of the payment slip to icitonline@gmail.com)

Bank Name: CANARA Bank, Shivajinagar Branch, Pune

Account Name: ICIT Pvt. Ltd.

Account Number: 53211010006135

IFSC Code: CNRB0015321

PAN: AAACI7348N

Please submit copies of your semester wise mark lists OR copy of the final Degree Certificate.

Address for correspondence:

ICIT Pvt. Ltd.,
Department of Electronic and
Instrumentation Science,
Savitribai Phule Pune University,
Ganeshkhind road,
Pune 411 007.

❖ **PG Diploma Course in Embedded System Design**

✓ CET Dates : Aug 27th and 28th Aug 2022

✓ Commencement of Course : 12th Sept.2022

❖ Last date of application : 25th Aug ,2022